

ELECTROSTATIC DISCHARGE (ESD) TOOL FOR ELECTRONIC DEVICE UNDER TEST (DUT) BOARDS

ABSTRACT OF THE DISCLOSURE

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An anti-electrostatic discharge (ESD) tool for engaging electronic device under test (DUT) boards whereby ESD damage to the tested devices is prevented. The tool includes an aluminum support frame, guides on opposing edges of one side of the frame for slidably receiving a DUT board, and at least one electrical shorting connector extending from the frame and electrically connecting and shorting socket connectors and leads of electronic devices when the DUT board is inserted into the guides. The electrical shorting connector preferably comprises an array of fine wire brushes which have sufficient rigidity and flexibility for engaging solder points on the DUT board for the socket connectors.

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